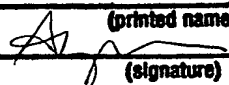


UNBLIND EQUALIZER ARCHITECTURE  
FOR DIGITAL COMMUNICATION SYSTEMS

ABSTRACT OF THE DISCLOSURE

5           A method and apparatus for equalizing digital data signals is disclosed. The  
method comprises the steps of demodulating and decoding an input signal having input  
data to produce a data output, remodulating the data output to produce a pseudo-training  
sequence including an idealized input signal, and generating equalizer parameters from  
the pseudo-training sequence. The apparatus comprises a demodulator for demodulating  
10   an input signal to produce a data output, a modulator, communicatively coupled to the  
demodulator, for remodulating the data output to produce a pseudo-training sequence  
including an idealized input signal, and a parameter generation module, communicatively  
coupled to the modulator for generating equalizer parameters from the pseudo-training  
sequence.

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